10_100_1000 Mbps Tri-mode Ethernet MAC Specification

Author: Jon Gao

gaojon@yahoo.com

Rev. 0.1

January 25, 2006

Reg Description

Register list

Reg Name	Operaion	Address	Default Value
Tx_Hwmark	R/w	7'd000	16'h001e
Tx_Lwmark	R/w	7'd001	16'h0019
pause_frame_send_en	R/w	7'd002	16'h0000
pause_quanta_set	R/w	7'd003	16'h0000
IFGset	R/w	7'd004	16'h0012
FullDuplex	R/w	7'd005	16'h0001
MaxRetry	R/w	7'd006	16'h0002
MAC_tpra(M) Tj03976 400mt_addren22Tj	R/w	7'd007	16'h0000

[redacted]

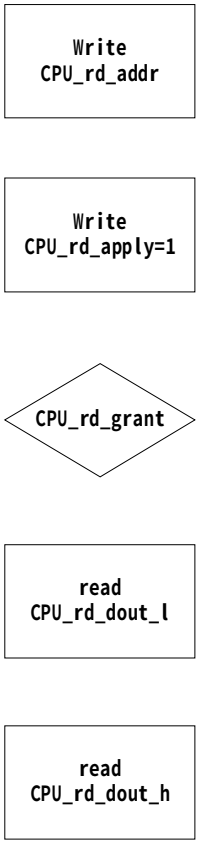
Tj10wTj10

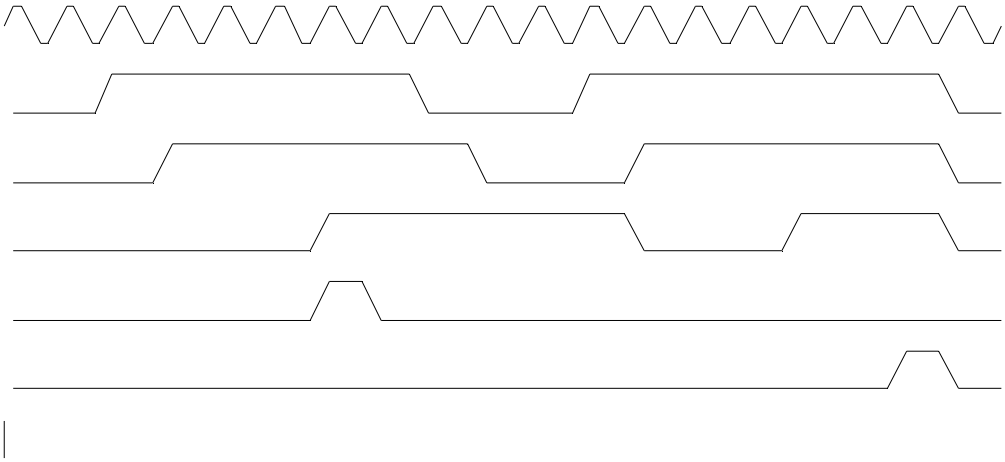
 Prei i onnTj-0.15552 Tc (e)aTj0.24336 Tc (r) Tj-0.15 Tc (i)y Tj/F1 10.08 Tf

Twwww

IFG

TS.84T37.331.08lS /im08RGm /im888g /FwDo QBT36.72 0.48 r0.72 f89.76 61.68 TD ( ) /im088g 178TD (1008 Tf84.84T5/2


Write
CPU_rd_addr
Write
CPU_rd_apply=1
CPU_rd_grant
read
CPU_rd_dout_l
read
CPU_rd_dout_h



ou2fBT.481|29084 5631563713(ou2fBT.481|29084 5671563713(ou2fBT.481|29084 5637156373(ou2fBT.481|29084

Index

This section contains an alphabetical list of helpful document entries with their corresponding page numbers.